

	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
1	US 20040179547 A1	20040916	44	Method and apparatus for adaptive processing gain for multiple source devices in a communications system	370/465	
2	US 20040179510 A1	20040916	47	Method and apparatus for source device synchronization in a communication system	370/350	
3	US 20040165508 A1	20040826	57	Device and method for recording data to optical disk using recording pulse with corrected edge position	369/59.11	
4	US 20040165507 A1	20040826	57	Device and method for recording data to optical disk using recording pulse with corrected edge position	369/59.11	
5	US 20040165506 A1	20040826	57	Device and method for recording data to optical disk using recording pulse with corrected edge position	369/59.11	
6	US 20040165505 A1	20040826	56	Device and method for recording data to optical disk using recording pulse with corrected edge position	369/59.11	
7	US 20040080293 A1	20040429	37	Rotation drive control circuit of multiphases direct current motor and the start-up method thereof	318/459	318/254
8	US 20040080284 A1	20040429	25	Intensity modulator for light source such as AC lamp	315/291	315/247
9	US 20030164164 A1	20030904	19	IGNITION COIL INTEGRATED ION SENSE WITH COMBUSTION AND KNOCK OUTPUTS	123/606	123/625; 73/35.08
10	US 20030123590 A1	20030703	24	Control and supervisory signal transmission system	375/354	
11	US 20030122759 A1	20030703	78	Display apparatus, and image signal processing apparatus and drive control apparatus for the same	345/89	345/690
12	US 20030063108 A1	20030403	43	Drive signal generator and image display apparatus	345/690	

	<b>Inventor</b>
<b>1</b>	Kuffner, Stephen et al.
<b>2</b>	Kuffner, Stephen et al.
<b>3</b>	Gushima, Toyoji et al.
<b>4</b>	Gushima, Toyoji et al.
<b>5</b>	Gushima, Toyoji et al.
<b>6</b>	Gushima, Toyoji et al.
<b>7</b>	Kurosawa, Minoru et al.
<b>8</b>	Gyoten, Takaaki
<b>9</b>	Butler, Raymond O. JR.
<b>10</b>	Saitou, Yoshitane et al.
<b>11</b>	Abe, Naoto et al.
<b>12</b>	Isono, Aoji et al.

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef
13	US 20030058146 A1	20030327	17	Synchronized pulse width modulator	341/143	
14	US 20030021497 A1	20030130	28	Light beam deflecting apparatus, image forming apparatus utilizing the same and drive method therefor	382/323	
15	US 20020195966 A1	20021226	52	Drive circuit, display device, and driving method	315/169.3	315/169.2
16	US 20020135666 A1	20020926	24	Clock-generating circuit and image-forming apparatus having a function of canceling unevenness of scanning-light amount	347/249	
17	US 6882329 B2	20050419	41	Drive signal generator and image display apparatus		345/204
18	US 6867555 B2	20050315	24	Intensity modulator for light source such as AC lamp	315/291	315/248; 315/307; 315/DIG.4
19	US 6784914 B2	20040831	22	Clock-generating circuit and image-forming apparatus having a function of canceling unevenness of scanning-light amount	347/247	
20	US 6757084 B2	20040629	36	Image reading apparatus and control method thereof	358/509	358/504
21	US 6732217 B1	20040504	43	Control and supervisory signal transmission system	710/306	710/106; 710/110
22	US 6721255 B1	20040413	55	Device and method for recording data to optical disk using recording pulse with corrected edge position	369/59.12	369/124.04; 369/47.19
23	US 6664908 B2	20031216	16	Synchronized pulse width modulator	341/143	341/155; 341/156
24	US 6615811 B1	20030909	18	Ignition coil integrated ion sense with combustion and knock outputs	123/606	123/625; 73/35.08
25	US 6473110 B1	20021029	18	Image recording apparatus simultaneously recording a plurality of line images with a plurality of light beams	347/235	347/250

	<b>Inventor</b>
<b>13</b>	Sundquist, Robert S. et al.
<b>14</b>	Kandori, Atsushi et al.
<b>15</b>	Aoki, Tadashi et al.
<b>16</b>	Takaki, Kouichi et al.
<b>17</b>	Isono; Aoji et al.
<b>18</b>	Gyoten; Takaaki
<b>19</b>	Takaki; Kouichi et al.
<b>20</b>	Kurita; Mitsuru et al.
<b>21</b>	Nishikido; Kenji
<b>22</b>	Gushima; Toyoji et al.
<b>23</b>	Sundquist; Robert S. et al.
<b>24</b>	Butler, Jr.; Raymond O.
<b>25</b>	Matsuo; Toshihisa

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef
26	US 6377234 B1	20020423	14	Liquid crystal display circuit using pulse width and frame modulation to produce grayscale with continuity	345/89	345/94
27	US 6195087 B1	20010227	15	Method and device for preventing the jumping phenomenon of an OSD display region on a monitor screen	345/213	345/18; 345/212; 345/214; 345/29
28	US 6141357 A	20001031	26	Method and apparatus for controlling mobile receivers	370/507	398/1; 398/118; 398/127; 398/154; 398/76; 398/9; 455/502
29	US 6037734 A	20000314	34	Motor velocity controlling method employing detection of all side edges of phase signals of an encoder to generate control target values for updating a motor control command	318/602	318/254
30	US 5995465 A	19991130	16	Digital signal reproducing apparatus	369/47.28	369/124.05; 369/124.13; 369/47.35; 369/59.22
31	US 5915108 A	19990622	9	Method of measurement and compensation of an inaccurate clock signal	713/500	
32	US 5852506 A	19981222	35	Method and apparatus for controlling program start/stop operations	398/1	398/155; 455/450; 455/463; 455/464; 455/500; 455/509
33	US 5850610 A	19981215	35	Method and apparatus for providing zoned communications	455/512	455/443; 455/502; 455/503

	<b>Inventor</b>
<b>26</b>	Nogawa; Shinichi
<b>27</b>	Wu; Tsung-Hsun
<b>28</b>	Testani; Alan John et al.
<b>29</b>	Toyomura; Yuji
<b>30</b>	Hayashi; Hideki et al.
<b>31</b>	Frey; Michael James et al.
<b>32</b>	Testani; Alan John et al.
<b>33</b>	Testani; Alan John et al.

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef
34	US 5818814 A	19981006	34	Method and apparatus for synchronizing and controlling remote receiver	370/212	370/328; 370/503; 375/238; 375/356; 455/443; 455/502
35	US 5774702 A	19980630	57	Integrated circuit having function blocks operating in response to clock signals	713/501	
36	US 5751770 A	19980512	11	Data transmission system	375/238	370/205; 375/369
37	US 5739842 A	19980414	22	Image forming method and apparatus using rotated screen with pulse width modulation	347/252	347/132; 347/134; 347/135; 347/247
38	US 5731672 A	19980324	28	Control apparatus of DC servo motor	318/293	318/599; 318/6; 388/811
39	US 5537038 A	19960716	48	Magnetic flux measuring method and apparatus for detecting high frequency components of magnetic flux with high speed orientation	324/253	324/234; 324/237
40	US 5519361 A	19960521	39	Pulse width controller adopting feedback PWM integration system employing carrier-synchronous signal	332/109	375/238
41	US 5477257 A	19951219	22	Image forming method and apparatus using rotated screen with pulse width modulation	347/135	347/132; 347/134; 358/296; 358/300
42	US 5446550 A	19950829	93	Image forming apparatus with random density pattern growth	358/3.19	347/131; 358/3.09; 358/3.26
43	US 5264802 A	19931123	8	Circuit and method for monitoring phase error modulation	327/3	327/231; 327/256
44	US 5200675 A	19930406	19	Commutation circuit for a brushless D.C. Motor	318/254	318/138; 318/439

	<b>Inventor</b>
<b>34</b>	Testani; Alan John et al.
<b>35</b>	Mitsuishi; Naoki et al.
<b>36</b>	Tatara; Hirokazu
<b>37</b>	Murata; Kazuyuki
<b>38</b>	Miyaguchi; Ryuichi
<b>39</b>	Ando; Seigo
<b>40</b>	Kim; Jin-bok
<b>41</b>	Murata; Kazuyuki
<b>42</b>	Maekawa; Shinichiro et al.
<b>43</b>	Beg; Mirza A. et al.
<b>44</b>	Woo; Kwangjoon



	Document ID	Issue Date	Pages	Title	Current OR	Current XRef
45	US 5034967 A	19910723	23	Metastable-free digital synchronizer with low phase error	375/373	327/141; 375/359; 375/371
46	US 5018027 A	19910521	11	Method of and means for providing information to edit a video tape	386/65	
47	US 4799218 A	19890117	12	Network system	370/438	340/825.2; 340/825.52
48	US 4715031 A	19871222	26	Vehicular data transfer communication system	370/462	340/825.5; 370/475
49	US 4712211 A	19871208	23	Network system utilizing an intermediate synchronizations signal and predetermined code string patterns	370/514	375/357
50	US 4701796 A	19871020	16	Synchronization signal generating circuit	348/530	
51	US 4644232 A	19870217	18	Method of and an apparatus for controlling a plurality of DC motors	318/66	318/101; 318/51; 318/91
52	US 4599685 A	19860708	17	Control circuit for power converter apparatus	363/41	363/127; 363/129; 363/37; 363/87; 363/89
53	US 4553247 A	19851112	38	Telemetry system with signal booster for digital data transmission through a transmission line	375/214	178/70R; 340/870.13; 370/501; 375/257
54	US 4468785 A	19840828	37	Telemetry system with signal booster for digital data transmission through a transmission line	375/214	178/70R
55	US 4459612 A	19840710	14	Vertical synchronizing signal detecting circuit	348/529	
56	US 4415920 A	19831115	15	Image signal processing unit	348/630	358/520
57	US RE30785 E	19811027	21	Microcomputer terminal system	345/472	
58	US 4222270 A	19800916	14	Gyroscope rate range switching and control system	73/504.11	318/648

	<b>Inventor</b>
<b>45</b>	Cox; William M. et al.
<b>46</b>	Roggendorf; Peter
<b>47</b>	Sakagami; Atsushi et al.
<b>48</b>	Crawford; Robert D. et al.
<b>49</b>	Suzuki; Tadashi et al.
<b>50</b>	Kamiya; Kiyoshi
<b>51</b>	Nojiri; Yuukou et al.
<b>52</b>	Hombu; Mitsuyuki et al.
<b>53</b>	Harris; Robert W.
<b>54</b>	Harris; Robert W.
<b>55</b>	Shinkai; Mitsutoshi et al.
<b>56</b>	Kato; Yuzo et al.
<b>57</b>	Lovercheck; Lawrence R. et al.
<b>58</b>	Allen; Arnold R.

	Document ID	Issue Date	Page s	Title	Current OR	Current XRef
59	US 4138632 A	19790206	12	Pulse width modulator digital servo system	318/599	375/238; 388/811; 388/912
60	US 3973244 A	19760803	19	Microcomputer terminal system	345/472	
61	US 3967173 A	19760629	26	Transistor bridge inverter motor drive having reduced harmonics	318/811	363/132; 363/41
62	US 3644677 A	19720222	8	SIGNAL DEMODULATING SYSTEM	348/471	348/638; 370/212; 370/498; 375/238; 375/359; 386/36; 386/40

	<b>Inventor</b>
<b>59</b>	Pauwels; Michael A. et al.
<b>60</b>	Lovercheck; Lawrence R. et al.
<b>61</b>	Stich; Frederick A.
<b>62</b>	Cecchin; Gildo et al.